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APPLICATION NO.	FI	ILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/605,673	73 10/16/2003		Lannie R. Bolde	FIS920030276US1	2672
29371	7590	09/28/2004		EXAMINER	
CANTOR (COLBUR	N LLP	JOHNSON, JONATHAN J		
55 GRIFFIN	ROAD S	OUTH			
BLOOMFIELD, CT 06002			ART UNIT	PAPER NUMBER	
				1725	

DATE MAILED: 09/28/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)				
Office Action Summary	10/605,673	BOLDE ET AL.				
,	Examiner Jonathan Johnson	Art Unit				
The MAILING DATE of this communication app						
Period for Reply	cars on the cover sheet with the c	onespondence address				
A SHORTENED STATUTORY PERIOD FOR REPLY THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply If NO period for reply is specified above, the maximum statutory period we Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	86(a). In no event, however, may a reply be time within the statutory minimum of thirty (30) days will apply and will expire SIX (6) MONTHS from cause the application to become ABANDONEI	ely filed s will be considered timely. the mailing date of this communication. O (35 U.S.C. § 133).				
Status						
1) Responsive to communication(s) filed on 16 Oc	ctober 2003					
	action is non-final.					
,		secution as to the merits is				
•	3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.					
·	reparts quayre, 1000 o.b. 11, 10	5 5.5. 210.				
Disposition of Claims						
4) Claim(s) <u>1-20</u> is/are pending in the application.						
4a) Of the above claim(s) <u>1-8</u> is/are withdrawn f	rom consideration.					
5) Claim(s) is/are allowed.						
6)⊠ Claim(s) <u>9-19</u> is/are rejected.						
7)⊠ Claim(s) <u>20</u> is/are objected to.						
8) Claim(s) <u>1-20</u> are subject to restriction and/or e	lection requirement.					
Application Papers						
9) The specification is objected to by the Examiner						
10) The drawing(s) filed on is/are: a) acce		yaminer				
Applicant may not request that any objection to the o	· · · · · · · · · · · · · · · · · · ·					
Replacement drawing sheet(s) including the correcti	•	• •				
11) The oath or declaration is objected to by the Exa						
,	annion. Note the attached emice	7.00.07.07.107.17.1.0.102.				
Priority under 35 U.S.C. § 119						
 12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of: 1. Certified copies of the priority documents 2. Certified copies of the priority documents 3. Copies of the certified copies of the priori application from the International Bureau * See the attached detailed Office action for a list of 	have been received. have been received in Application ty documents have been receive (PCT Rule 17.2(a)).	on No d in this National Stage				
Attachment(s)						
1) Notice of References Cited (PTO-892)	4) 🔲 Interview Summary (PTO-413)				
2) Notice of Draftsperson's Patent Drawing Review (PTO-948)	Paper No(s)/Mail Da	te				
3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date 101603.	5) Notice of Informal Pa 6) Other:	atent Application (PTO-152)				

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DETAILED ACTION

Election/Restrictions

Restriction to one of the following inventions is required under 35 U.S.C. 121:

- I. Claims 1-8 are drawn to the method for separating a semiconductor device,
 classified in class 29, subclass various.
- II. Claims 9-20 are drawn to an apparatus for removing a chip from a substrate, classified in class 228, subclass 264.

The inventions are distinct, each from the other because of the following reasons:

Inventions Group I and Group II are related as process and apparatus for its practice. The inventions are distinct if it can be shown that either: (1) the process as claimed can be practiced by another materially different apparatus or by hand, or (2) the apparatus as claimed can be used to practice another and materially different process. (MPEP § 806.05(e)). In this case the apparatus can be used in adhesive connections.

Because these inventions are distinct for the reasons given above and the search required for Group I is not required for Group II, restriction for examination purposes as indicated is proper.

During a telephone conversation with Jim Merrick on 9-20-04 a provisional election was made without traverse to prosecute the invention of Group II, claims 9-20. Affirmation of this election must be made by applicant in replying to this Office action. Claims 1-8 are withdrawn from further consideration by the examiner, 37 CFR 1.142(b), as being drawn to a non-elected invention.

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Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 9-20 are rejected under 35 U.S.C. 102(b) as being anticipated by Olson et al. (5,707,000). Olsen et al. teach an apparatus fro removing a chip having a shearing element configured to apply a loading force (fig. 3, item 55); a fixture having a loading element for placing the substrate (fig. 3, item 65), wherein the solder connections intermediate the chip and the substrate are heated to a predetermined temperature by applying a heat source to a surface of the substrate distal from the chip (col. 8, ll. 1-10, where the furnace would necessarily apply heat to the surface of the substrate distal from the chip); the substrate is loaded onto a seat in an upper element with the chip extending through a window (fig. 3, item 67 and area supporting item 60); the shearing element is releasably clamped to the chip using a drive screw to operate the claws that clamp edges defining the chip (fig. 3, items 51, 59, and 67); and a loading force is provided by the shearing element for biasing the chip away from the substrate (figure 3, item 55); wherein the shearing element provides a gravitational force on the chip and is a heatsink in thermal communication with the chip (Figure 3, item 55 and col. 3, ll. 30-40); wherein the claimed temperature gradient is established (col. 8, ll. 1-10); wherein the heat source is a controlled heat source applied to the surface of the substrate (col. 8, 11. 1-10); wherein the reflow furnace is capable of obtaining a temperature below the melting temperature of the solder (col. 8, ll. 1-10); wherein the fixture includes a nest below the window adapted and located for catching the chiop

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screw is manipulated (figure 3, item 57 and 69).

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sheared (figure 3, item 65); wherein the shearing element comprises yoke block (fig. 3, item 55); a plurality of jaws, a plurality of jaws pivotally coupled to a corresponding side of a contiguous sides defining yoke block (figure 3, items 57 and 61); a drive screw threadably engaged with the yoke block through one of two opposing sides normal to said contiguous sides defining said yoke block (figure 3, item 51, longitudinal axis if the screw); wherein said plurality of jaws each configured to clamp edges defining the semiconductor device to said yoke block when the drive

Allowable Subject Matter

Claim 20 is objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

The following is a statement of reasons for the indication of allowable subject matter:

The prior art of record does not suggest or teach the particular drive cam.

Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jonathan Johnson whose telephone number is 571-272-1177. The examiner can normally be reached on M-Th 7:30 AM-5:30 PM.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tom Dunn can be reached on 571-272-1171. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Jonathan Johnson

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